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SystemVerilog OOP - Polymorphism

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Integrated Circuit (IC) Fabrication VLSI

~~Fabrication Process~~ What is

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VLSI?(Explained!!!) Semiconductor
Materials (Ge, Si, GaAs)
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N+ P N P+ N+ P Aluminum is
deposited on the wafer. Photoresist is
then spun onto the wafer and mask #6
is used to define the wiring regions.

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values of $n_i = 1.45 \times 10^{10} \text{ cm}^{-3}$, $\mu_n =$

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$1500 \text{ cm}^2 \text{ volt}^{-1} \text{ sec}^{-1}$ and $\mu_p = 500$
 $\text{cm}^2 \text{ volt}^{-1} \text{ sec}^{-1}$, we have: τ_i
 $= 4.64 \times 10^{-6} \text{ cm}$ and $\tau_{\text{min}} = 4.02 \times 10^{-6} \text{ cm}$

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Plummer, Deal and Griffin P N+ N P+ P+ The P+ isolation regions are driven down to the P substrate to laterally isolate the devices. SiO₂ is grown on the surface during this drive-in. Note that the buried layer

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