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A presentation of circuit synthesis and circuit simulation using VHDL (including VHDL 2008), with an emphasis on design examples and laboratory exercises. This text offers a comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits. It focuses on the use of VHDL rather

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Simulation tools can be an important part of design optimization as designers can experiment with different components and topologies before integrating circuit blocks into a physical layout. When circuit simulations are needed as part of VLSI layout, the right set of circuit simulation tools will aid design optimization using parameter sweeps.

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the complexity of the analog
circuit, analog design teams
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This comprehensive treatment of
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design and simulation of real,
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New features include all VHDL-2008 constructs, an extensive review of digital circuits, RTL analysis, and an unequaled collection of VHDL examples and exercises.

~~Circuit Design with VHDL, third edition (The MIT Press ...~~

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VHDL Book Description: A

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~~VHDL, Second Edition ...~~

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A presentation of circuit synthesis and circuit simulation using VHDL (including VHDL 2008), with an emphasis on design examples and laboratory exercises. This text offers a comprehensive treatment of VHDL and its applications to the

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