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The third and fourth instructions say that, at any moment the automaton may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that in state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. Finally, the sixth instruction says that the machine may move from state q, for each symbol 1 read, one A is popped. of a single ${\mathtt Z}$.

Pushdown automaton - Wikipedia

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Cardboard Automata - Exploratorium

2: a machine or control mechanism designed to follow automatically a predetermined sequence of operations 3: an individual who acts in a mechanical fashion He is an unfeeling automaton.

Automaton | Definition of Automaton by Merriam-Webster

On the processing architecture, a given instruction may specify: particular registers (for arithmetic, addressing, or control functions) particular memory locations (or offsets to them) particular addressing modes (used to interpret the operands)

Instruction set architecture - Wikipedia

The collection of machine instructions in main memory is called a machine language program. Machine code or machine language is a set of instruction performs a very specific task, such as a load, a jump, or an ALU operation on a unit of data in a CPU register or memory. Every program directly executed by a CPU is made up of a series of such instructions. The general format of a machine instruction is

Machine Instructions - GeeksforGeeks

In computer programming, machine code, consisting of machine language instructions, is a low-level programming language used to directly control a computer's central processing unit (CPU). Each instructions, is a low-level programming language used to directly control a computer's central processing unit (CPU). (ALU) operation on one or more units of data in the CPU's registers or memory.

Machine code - Wikipedia

The J instruction has the most space for an immediate value, because addresses are large numbers. J instructions have the following machine-code format:

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A Turing machine is a mathematical model of computation that defines an abstract machine, which manipulates symbols on a strip of tape according to a table of rules. Despite the model's simplicity, given any computer algorithm, a Turing machine capable of simulating that algorithm's logic can be constructed.. The machine operates on an infinite memory tape divided into discrete "cells".

Turing machine - Wikipedia

The instructions are usually part of an executable program, often stored as a computer file and executed on the processor. The x86 instruction set has been extended several times, introducing wider registers and datatypes as well as new functionality.

x86 instruction listings - Wikipedia

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It is called a machine instruction because the assembler translates it into the machine language code that the computer can run. Machine instructions are described in Machine instruction statements. Parent topic: Assembler language

Machine instructions - IBM

The offset stored in a beq (or bne) instruction is the number of instruction after the beq instruction after the beq instruction after the difference between the address associated with the label and the PC, divided by four. offset = (addrFromLabelTable - PC) / 4

MIPS Instruction Formats - Kalamazoo College

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